SSi Link&Locate 386 and Link&Locate x86

- 32-Bit Protected-Mode Development
- 16-Bit Real- and Protected Mode Development
- C and Full C++ Support
- OMF and SPF Format Support
- User Definable Interfaces
- Configurable Monitors

AnnaSoft is a licensed distributor of SSi tools such as Link&Locate x86 and Link&Locate 386, which is available separately or in a kit that includes the SoftProbe Debugger as well.

Link&Locate x86 is an absolute linker/locator that generates code for embedded real-mode 16-bit Intel 8086/186 and 286/386/486 and Pentium® processor applications. It accepts object and library files generated by the Microsoft C/C++ Optimizing Compiler and MASM Assembler (both sold separately from AnnaSoft). Link&Locate 86 links C++ object files, but generates mangled C++ symbols in the output OMF file and demangled C++ symbols in the map file. The Intel OMF-86 file format only supports C and assembly symbols.

Link&Locate 386 is an absolute linker/locator/builder for embedded 32-bit protected-mode and 16-bit protected-mode Intel 286/Intel386™/Intel486™ and Pentium® processor applications. It has the flexibility to define and construct segment descriptors, local, global, and interrupt descriptor tables, task, interrupt, call and trap gates, and task state segments (TSS). Link&Locate 386 builds Intel OMF-286 and OMF-386 bootloadable absolute files

for embedded system development. Link&Locate 386 generates Binary, Intel Hex and Intel Extended Hex files for PROM Programmers. It supports development of applications featuring many of the currently available RTOS kernels. Link&Locate 386 includes full assembly and C source code to the reset and start-up code with DOS-Independent run-time support, C, C++, and floating point examples. It supports flat memory model and multi-segmented memory models suitable for building multi-tasking and kernel-based applications. Link&Locate also supports compressed initialized data to minimize ROM overhead.

Both Linker/Locators do linking and locating in one pass to reduce the like time in very large applications. Incremental linking is supported to reduce the like time with very large applications. Both of these products generate full debug information for symbolic and source level debugging with embedded debuggers like In-Circuit Emulators, Logic Analyzers, Remote Target Debugger and Simulators.

PROCESSORS SUPPORTED:
Intel386 SX/DX/EX, Intel486 and Pentium® processors

DEVELOPMENT PLATFORMS:
MS-DOS and Windows* Compatible Systems

AVAILABILITY:
Now

CONTACT:
AnnaSoft
11838 Bernardo Plaza Court
San Diego, CA 92128-2414
Phone: (800) 690-3870
         (619) 674-6155
FAX: (619) 673-1432
e-mail: annasoft@annasoft.com
BBS: (619) 673-1773
WWW: http://www.annasoft.com

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